Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	87	((read$3 or writ$3 or terminat$3) near3 burst) and (EDC or (error adj2 detect$4 adj2 correct$4) or ECC) and (SRAM or SDRAM or (synchronous adj2 random adj1 access adj1 memory)) and FIFO	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/01/04 15:05
L2	6	((read$3 or writ$3 or terminat$3) near3 burst) and (EDC or (error adj2 detect$4 adj2 correct$4) or ECC) and ((SRAM or SDRAM or (synchronous adj2 random adj1 access adj1 memory)) adj2 array$1) and FIFO	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/01/04 15:09
L3	12	((read$3 or writ$3 or terminat$3) near3 burst) and (EDC or (error adj2 detect$4 adj2 correct$4) or ECC) and ((SRAM or SDRAM or (synchronous adj2 random adj1 access adj1 memory)) adj2 array$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/01/04 15:08
L4	87	((read$3 or writ$3 or terminat$3) near3 burst) and (EDC or (error adj2 detect$4 adj2 correct$4) or ECC) and (SRAM or SDRAM or (synchronous adj2 random adj1 access adj1 memory)) and FIFO	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/01/04 15:09
L5	42	((terminat$3) near3 burst) and (EDC or (error adj2 detect$4 adj2 correct$4) or ECC) and (SRAM or SDRAM or (synchronous adj2 random adj1 access adj1 memory)) and FIFO	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/01/04 15:10
L6	38	((terminat$3) near3 burst) and (EDC or (error adj2 detect$4 adj2 correct$4)) and (SRAM or SDRAM or (synchronous adj2 random adj1 access adj1 memory)) and FIFO	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/01/04 15:20
L7	38	((terminat$3) near3 burst) and (EDC or (error adj2 detect$4 adj2 correct$4)) and ((SRAM or SDRAM or (synchronous adj2 random adj1 access adj1 memory) adj2 array)) and FIFO	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/01/04 15:34
L8	1	(terminat$3 near3 burst) and (read$3 near3 burst) and (EDC or (error adj2 detect$4 adj2 correct$4)) and ((SDRAM or (synchronous adj2 random adj1 access adj1 memory) adj2 array)) and FIFO	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/01/04 15:39

| L9 | 4 | (terminat$3 near3 burst) and (read$3 near3 burst) and (EDC or (error adj2 detect$4 adj2 correct$4)) and ((SDRAM or (synchronous adj2 random adj1 access adj1 memory) adj2 array)) and FIFO | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2006/01/04 15:40 |